

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,200,740 B2
APPLICATION NO. : 09/849822
DATED : April 3, 2007
INVENTOR(S) : G. Glenn Henry and Thomas C. McDonald

Page 1 of 1

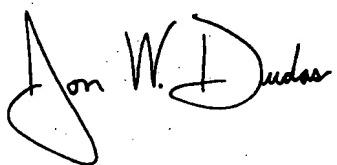
It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Title page, item [54] and Col 1, Delete the TITLE and change it to:

MICROPROCESSOR WITH SELECTIVELY OVERRIDING FIRST AND SECOND PREDICTIVE CALL/RETURN STACKS

Signed and Sealed this

Sixth Day of November, 2007



JON W. DUDAS
Director of the United States Patent and Trademark Office